

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Prior Application: C. OKAMOTO et al  
Serial No. 09/856,758  
Filed: September 14, 2001

Group Art Unit: 2827  
Examiner: J. Vigushin  
For: METHOD OF MOUNTING ELECTRONIC CIRCUIT  
CHIP

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

October 20, 2003

Sir:

Prior to examination, please amend the above application  
as follows.